

## REMARKS

Claims 1-55 are pending in the present application. Applicant respectfully requests reconsideration of the present application in view of the following remarks.

### **I. Drawings**

Applicant acknowledges that formal drawings will be required when the present application is allowed.

### **II. Allowable Subject Matter**

Applicant gratefully acknowledges allowance of claims 38-45.

Claims 14-16, 18-24, 26, and 37 were objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims. As set forth below, Applicant submits that the base claims from which these claims respectively depend are allowable. Accordingly, Applicant requests withdrawal of the objection to these claims.

### **III. Claim Rejections – 35 USC § 103**

Claims 1-4, 10-13, 17, 25, 27-31, and 46-54 were rejected as being unpatentable over U.S. Patent No. 5,749,086 to Ryan and U.S. Patent No. 6,381,684 to Hronik, et al. ("Hronik"). Applicant submits that the Hronik patent and the present application were, at the time of the invention, both owned by the same company, Integrated Device Technology, Inc. Accordingly, pursuant to the provisions of 35 USC § 103 (c), the Hronik patent is disqualified as a reference under §§102 (e), (f), and (g) for purposes of rejections under § 103.

Withdrawal of this rejection is respectfully requested.

Claims 5-9, 32-36, and 55 were rejected as unpatentable over Ryan, Hronik, and U.S. Patent No. 5,987,570. Applicant submits that these claims are allowable at least by their dependency on allowable claims. Withdrawal of this rejection is requested.

Applicant also traverses this rejection on the basis that Hronik is disqualified as a reference under §§102 (e), (f), and (g) for purposes of rejections under § 103 as set forth above.

#### **IV. Response to Arguments**

At paragraph 30 of the Office Action, it was recommended that Applicant amend claims 1 and 54. In response, Applicant submits that these claims are unambiguous.

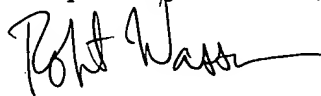
Accordingly, Applicant is not amending claim 1 or claim 54 at this time.

#### **CONCLUSION**

Applicant respectfully requests allowance of all pending claims. Should there be any questions concerning this response, the Examiner is invited to call the undersigned at (415) 217-6000.

EXPRESS MAIL LABEL NO:  
EV 259 160 113 US

Respectfully submitted,



Robert D. Wasson  
Reg. No.40,218

LAW OFFICES OF SKJERVEN MORRILL LLP  
Three Embarcadero Ctr., 28th Floor  
San Francisco, CA 94111

LAW OFFICES OF  
SKJERVEN MORRILL LLP  
San Jose, CA  
San Francisco, CA

Attachment A

Version of Claim showing changes made

The deletion is shown in brackets.

52. The memory circuit of claim 51 [a] further comprising:

an address bus for receiving a burst address; and

a control input terminal for receiving a read/write control signal for indicating a read burst or a write burst operation,

wherein the first read burst operation is initiated upon a rising edge of a clock cycle by asserting the read/write control signal to indicate a read burst operation and providing a first burst address at the address bus both prior to the rising edge of the clock cycle, the first and second data items representing a burst of two data items corresponding to the first burst address.